

SPECIFICATIONS & HIGHLIGHTS

	ANAN – 10	ANAN – 100	ANAN – 100D
GENERAL			
Pricing (Introductory)	US\$ 1449.00 (Regular \$1795)	US\$ 1989.00 (Regular \$2495)	US\$2889.00 (Regular \$3495)
Warranty	1 Year Limited Manufacturer's Warranty*	1 Year Limited Manufacturer's Warranty*	1 Year Limited Manufacturer's Warranty*
FPGA	EP3C40	EP3C40	EP4CE115
Logic Elements	40K	40K	115K
On Board DSP Processing Capability	Yes (limited by FPGA LEs)	Yes (limited by FPGA LEs)	Extensive, including optional Soft Core processing
FLASH RAM	16MB	16MB	128MB
SRAM	-	-	32Mbit
Master Clock	122.88MHz	122.88MHz	122.88MHz

CLOCK Phase Noise @ 10KHz	-149dBc	-149dBc	-149dBc
10MHz Reference TCXO	± 2.5PPM	± 2.5PPM	± 2.5PPM
External 10Mhz Reference Input Ethernet	Yes	Yes	Yes
	GIGABIT	GIGABIT	GIGABIT
Modes	AM,FM,RTTY,CW,LSB,USB,DIGITAL (limited only by the application software)	AM,FM,RTTY,CW,LSB,USB,DIGITAL (limited only by the application software)	AM,FM,RTTY,CW,LSB,USB,DIGITAL (limited only by the application software)
Frequency Step	1 Hz	1 Hz	1 Hz
Antenna Ports(50 Ω)	3	3	3 + 1
I/O's	Seven User-Configurable Open-collector outputs	Seven User-Configurable Open-collector outputs	Seven User-Configurable Open-collector outputs
	Four user- configurable 12 bit buffered Analogue inputs	Four user- configurable 12 bit buffered Analogue inputs	Four user- configurable 12 bit buffered Analogue inputs
VNA***	10Khz - 55Mhz VNA	10Khz - 55Mhz VNA	10Khz - 55Mhz VNA

Housing	Customized Rugged Extruded Aluminum Housing	Rack Mount Housing	Rack Mount Housing
Dimensions(W×D×H)	135mm ×160mm ×68mm	445mm ×245mm ×89mm	445mm ×245mm ×89mm
Weight	1.5Kg (Approx)	4.5Kg (Approx)	4.5Kg (Approx)
RECEIVER			
Architecture	Direct Down Conversion	Direct Down Conversion	Direct Down Conversion
Number of ADC'S	1	1	2
Number of Receivers**	7 (4 Recommended)	7 (4 Recommended)	7 + 7
ADC Resolution	16 BIT	16 BIT	16 BIT
Frequency Coverage	10 KHz- 55 MHz	10 KHz- 55 MHz	10 KHz- 55 MHz
Dynamic Range	125 dBm	125dBm	125dBm
Diversity Reception	No	No	Yes
Noise Floor(500Hz Bandwidth)	-135 dBm	-135 dBm	-135 dBm

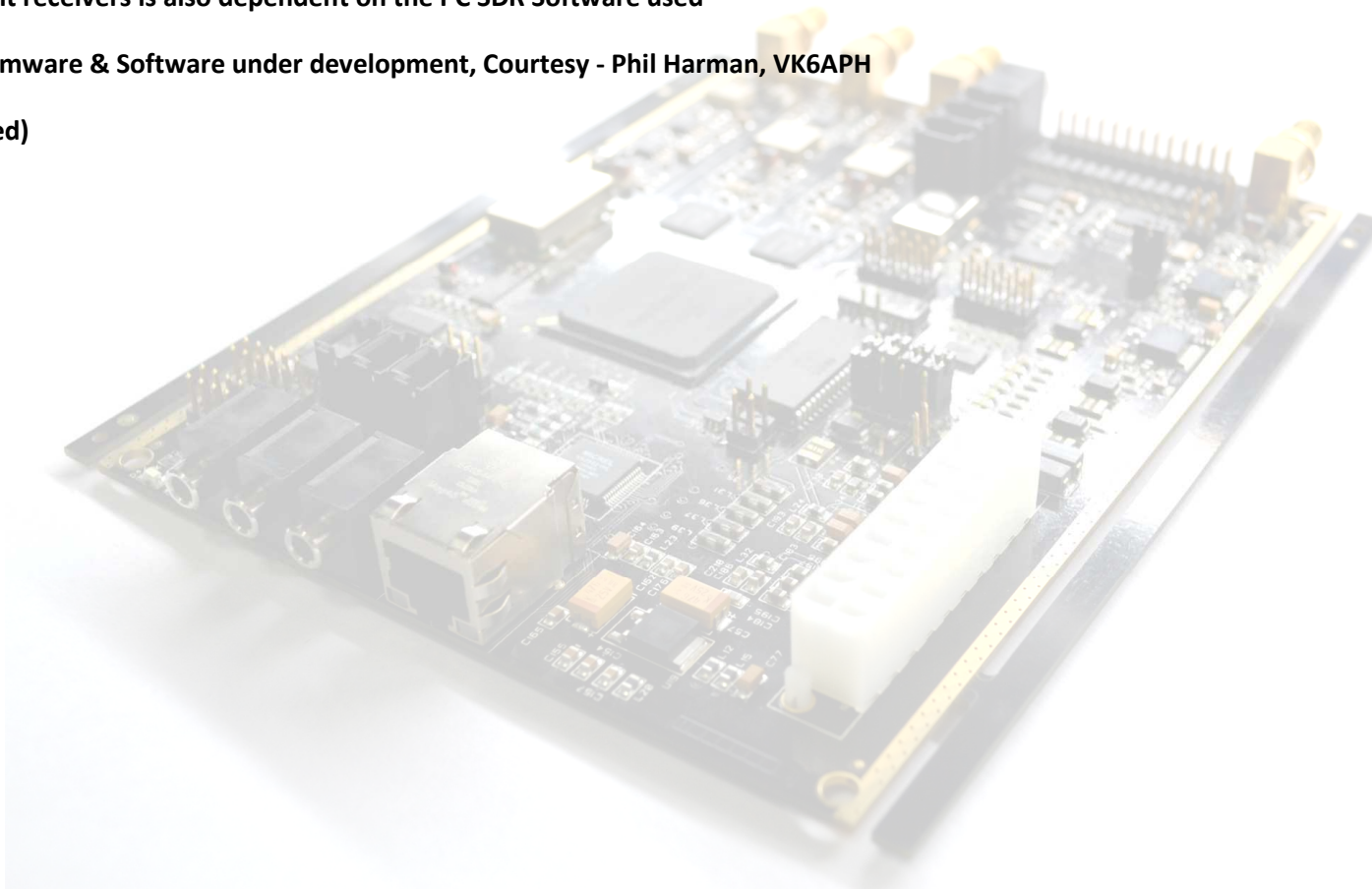
Receiver Front End Filtering	NA	Combination of 5 HPF & 7 LPF Banks	Combination of 5 HPF & 7 LPF Banks
Preamplifier	20 dB	20 dB, .5db NF, 20dB LNA for 6M	20 dB, .5db NF, 20dB LNA for 6M
Attenuator	Software selectable 31dB in 1dB steps	Software selectable 31dB in 1dB steps	Software selectable 31dB in 1dB steps
Spurious and Image rejection	Greater than 100 dB	Greater than 100 dB	Greater than 100 dB
Audio Amplifier	1W @ 8Ω	1W @ 8Ω	1W @ 8Ω
TRANSMITTER			
Architecture	Direct Up Conversion	Direct Up Conversion	Direct Up Conversion
Number of DAC's	1	1	1
RF Output	1-10W	1-100W	1-100W
Coverage	160M-6M	160M-6M	160M-6M
Modes	AM,FM,RTTY,CW,LSB,USB,DIGITAL (limited only by the application software)	AM,FM,RTTY,CW,LSB,USB,DIGITAL (limited only by the application software)	AM,FM,RTTY,CW,LSB,USB,DIGITAL (limited only by the application software)
Modulation	Digital Low Level	Digital Low Level	Digital Low Level
Carrier & Unwanted Sideband Suppression	Greater than -90dbc	Greater than -90dbc	Greater than -90dbc
Harmonic Suppression	Greater than 50dB(HF) Greater than 65dB(VHF)	Greater than 50dB(HF) Greater than 65dB(VHF)	Greater than 50dB(HF) Greater than 65dB(VHF)

* **Apache Labs Warranty Conditions apply**

** **Number of concurrent receivers is also dependent on the PC SDR Software used**

*****Open source VNA Firmware & Software under development, Courtesy - Phil Harman, VK6APH**

(Microphone not included)



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